

	Document ID	Issue Date	Page s	Title	Current OR	Current XRef	Inventor
1	US 20050002473 A1	20050106	42	Signal pulse detection scheme for use in real-time spectrum analysis	375/316		Kloper, David S. et al.
2	US 20040228424 A1	20041118	33	Receiver with analog barker detector	375/343		Baldwin, Keith R. et al.
3	US 20040184566 A1	20040923	8	Limit detector with hysteresis	375/340		Wahl, Mark A. et al.
4	US 20040156440 A1	20040812	36	System and method for real-time spectrum analysis in a communication device	375/259		Sugar, Gary L. et al.
5	US 20040028149 A1	20040212	32	Programmable integrated DiSEqC transceiver	375/316		Krafft, Stephen Edward et al.
6	US 20040028123 A1	20040212	59	System and method for real-time spectrum analysis in a radio device	375/224		Sugar, Gary L. et al.
7	US 20040022311 A1	20040205	46	Selectable-tap equalizer	375/229		Zerbe, Jared L. et al.
8	US 20030198304 A1	20031023	41	System and method for real-time spectrum analysis in a communication device	375/340		Sugar, Gary L. et al.
9	US 20030193996 A1	20031016	17	Transceiver for receiving and transmitting data over a network and method for testing the same	375/219		Iadanza, Joseph A.
10	US 20030174798 A1	20030918	19	High speed parallel link receiver	375/376		Pickering, Andrew J. et al.
11	US 20030147479 A1	20030807	50	System and method for shared decoding	375/341	714/795	Shah, Manish et al.
12	US 20030133530 A1	20030717	51	Apparatus and method for correcting signal imbalances for a multi-channel processing system	375/376		Peting, Mark
13	US 20030133519 A1	20030717	51	Forward tracing decoder apparatus and method	375/340	375/341	Shah, Manish et al.
14	US 20030133514 A1	20030717	50	Apparatus and method for decode arbitration in a multi-stream multimedia system	375/260		Lais, Eric et al.

	Document ID	Issue Date	Page s	Title	Current OR	Current XRef	Inventor
15	US 20030118130 A1	20030626	50	System and method for shared decoding using a data replay scheme	375/340	375/341	Greenberg, Mark et al.
16	US 20030118127 A1	20030626	51	System and method for concurrently demodulating and decoding multiple data streams	375/316	375/341	Peting, Mark
17	US 20030091094 A1	20030515	7	Using real random number generator as proof of time	375/130		Epstein, Michael A.
18	US 20030081709 A1	20030501	11	Single-ended IO with dynamic synchronous deskewing architecture	375/362		Ngo, Hiep P. et al.
19	US 20030043949 A1	20030306	3353	Radio frequency data communications device	375/374		O'Toole, James E. et al.
20	US 20030012259 A1	20030116	23	Communications system employing differential orthogonal modulation	375/140		Callaway, Edgar H. JR. et al.
21	US 20020191725 A1	20021219	17	Phase comparator	375/374		Dellow, Andrew
22	US 20020181571 A1	20021205	26	Method and apparatus for reducing signal processing requirements for transmitting packet-based data	375/222		Yamano, Larry C. et al.
23	US 20020154718 A1	20021024	10	Source synchronous receiver link initialization and input floating control by clock detection and DLL lock detection	375/354		Fong, Wai et al.
24	US 20020150189 A1	20021017	31	Apparatus and method for generating a distributed clock signal using gear ratio techniques	375/354		Ware, Frederick A. et al.
25	US 20020141527 A1	20021003	12	Locked loop circuit	375/376		Song, Hongjiang
26	US 20020141478 A1	20021003	146	Apparatus for initial power control for spread-spectrum communications	375/130		Ozluturk, Fatih M. et al.
27	US 20020131441 A1	20020919	165	Method of determining an end of a transmitted frame in a frame-based communications network	370/441	370/503; 375/343	Trachewsky, Jason Alexander et al.

	Document ID	Issue Date	Page s	Title	Current OR	Current XRef	Inventor
28	US 20020107979 A1	20020808	178	Computer data transmission over a telecommunications network	709/238	370/911; 375/222; 719/328	Sarnikowski, Scott et al.
29	US 20020085658 A1	20020704	33	Self-correcting multiphase clock recovery	375/376		Boerstler, David William
30	US 20020085657 A1	20020704	33	Multiphase clock recovery using D-type phase detector	375/373		Boerstler, David William
31	US 20020080886 A1	20020627	165	Method for selecting frame encoding parameters in a frame-based communications network	375/295		Ptasinski, Henry S. et al.
32	US 20020061050 A1	20020523	146	Method for initial power control for spread-spectrum communications	375/141	370/342	Ozluturk, Fatih M. et al.
33	US 20020051482 A1	20020502	146	Median weighted tracking for spread-spectrum communications	375/141	370/342	Lomp, Gary R.
34	US 20020036998 A1	20020328	147	Adaptive vector correlator for spread-spectrum communications	370/342	375/140	Lomp, Gary R.
35	US 20020027946 A1	20020307	147	Method for adaptive reverse power control for spread-spectrum communications	375/130	375/141	Ozluturk, Fatih M. et al.
36	US 20020021686 A1	20020221	147	System for using rapid acquisition spreading codes for spread-spectrum communications	370/342	375/140	Ozluturk, Fatih M. et al.
37	US 20020017949 A1	20020214	22	Apparatus and method for determining a pulse position for a signal encoded by a pulse modulation	329/313	375/340	Gfeller, Fritz et al.
38	US 20020012405 A1	20020131	62	Amplitude modulation detector	375/300	375/320	Lee, Jeung-In et al.
39	US 20010053174 A1	20011220	82	Spread spectrum localizers	375/130		Fleming, Robert Alan et al.

	Document ID	Issue Date	Page s	Title	Current OR	Current XRef	Inventor
40	US 20010033607 A1	20011025	82	Spread spectrum localizers	375/150		Fleming, Robert Alan et al.
41	US 20010022823 A1	20010920	22	Method and system for multi- protocol clock recovery and generation	375/359	375/354	Renaud, Pierre
42	US 6856206 B1	20050215	51	Method and apparatus for acquiring a frequency without a reference clock	331/25	327/2; 331/1A; 375/375	Perrott; Michael H.
43	US 6841983 B2	20050111	157	Digital signal to pulse converter and method of digital signal to pulse conversion	323/322	331/42; 365/226; 375/355	Thomas; John Carl
44	US 6836521 B2	20041228	29	Apparatus and method for generating a distributed clock signal using gear ratio techniques	375/354	327/144	Ware; Frederick A. et al.
45	US 6836515 B1	20041228	88	Multi-modulation radio communications	375/260	375/261; 375/269	Kay; Stanley E. et al.
46	US 6823483 B1	20041123	54	Physical coding sublayer for a multi-pair gigabit transceiver	714/701	375/219; 375/341; 375/348; 714/746	Creigh; John L.
47	US 6823025 B2	20041123	55	Amplitude modulation detector	375/320		Lee; Jeung-in et al.
48	US 6819728 B2	20041116	32	Self-correcting multiphase clock recovery	375/376	327/148; 327/157; 370/518; 375/374	Boerstler; David William

	Document ID	Issue Date	Page s	Title	Current OR	Current XRef	Inventor
49	US 6804316 B1	20041012	15	Methods and system for performing frame recovery in a network	375/368	375/365; 375/366; 375/377	Shectman; Nicholas
50	US 6803828 B2	20041012	8	Lock detector for phase locked loops	331/17	331/18; 331/23; 331/DIG .2; 375/340; 375/376	Tan; Loke Kun et al.
51	US 6801587 B2	20041005	NA	Circuit and system for extracting data	375/340	327/291; 375/354	Dosho; Shiro
52	US 6795491 B2	20040921	76	Spread spectrum localizers	375/213	342/125; 375/224; 702/97	Fleming; Robert Alan et al.
53	US 6785336 B1	20040831	61	Method and system for retrieving adaptation field data associated with a transport packet	375/240. 26		Kovacevic; Branko et al.
54	US 6778533 B1	20040817	60	Method and system for accessing packetized elementary stream data	370/392	370/542; 375/240. 02; 725/95	Kovacevic; Branko et al.
55	US 6775344 B1	20040810	19	Dropout resistant phase-locked loop	375/376	375/340	Buhler; Otto et al.
56	US 6775342 B1	20040810	53	Digital phase shifter	375/371	327/158; 327/161; 375/373; 375/376	Young; Steven P. et al.
57	US 6765954 B1	20040720	17	System and method for implementing a delta-sigma modulator integrity supervisor	375/220	375/224	Eichrodt; Christian et al.

	Document ID	Issue Date	Page s	Title	Current OR	Current XRef	Inventor
58	US 6760478 B1	20040706	98	Method and apparatus for performing two pass quality video compression through pipelining and buffer management	382/236	375/240.02; 375/240.13; 375/240.16; 382/239	Adiletta; Matthew James et al.
59	US 6748200 B1	20040608	24	Automatic gain control system and method for a ZIF architecture	455/234.1	375/345; 455/232.1; 455/234.2; 455/296; 455/324	Webster; Mark A. et al.
60	US 6748040 B1	20040608	40	Apparatus and method for effecting synchrony in a wireless communication system	375/354		Johnson; Timothy A. et al.
61	US 6744812 B2	20040601	19	Dual mode phone line networking modem utilizing conventional telephone wiring	375/222		Anne; Ramakrishna et al.
62	US 6741615 B1	20040525	37	Methods and apparatuses for synchronizing data conversion of sonet framed data	370/514	370/516; 370/539; 375/371	Patwardhan; Arvind Bhaskar et al.
63	US 6724817 B1	20040420	17	Adaptive image data compression	375/240.07		Simpson; Thomas Albert et al.
64	US 6721301 B2	20040413	146	Centroid tracking for spread-spectrum communications	370/342	375/152	Ozluturk; Fatih M. et al.
65	US 6718473 B1	20040406	32	Method and apparatus for reducing power consumption	713/320	375/327; 375/376	Mirov; Russell N. et al.

	Document ID	Issue Date	Page s	Title	Current OR	Current XRef	Inventor
66	US 6714605 B2	20040330	40	System and method for real-time spectrum analysis in a communication device	375/340	375/224; 375/228; 375/259	Sugar; Gary L. et al.
67	US 6711226 B1	20040323	11	Linearized digital phase-locked loop	375/371	327/152; 327/24; 370/516; 375/375	Williams; Bertrand J. et al.
68	US 6697350 B2	20040224	146	Adaptive vector correlator for spread-spectrum communications	370/342	375/150	Lomp; Gary R.
69	US 6674805 B1	20040106	72	System for controlling a clock signal for synchronizing a counter to a received value and method thereof	375/240. 28	375/240. 26	Kovacevic; Branko et al.

	Document ID	Issue Date	Page s	Title	Current OR	Current XRef	Inventor
70	US 6662367 B2	20031209	173	Poly-phase filters in multicarrier communication systems	725/105	341/100; 370/409; 370/478; 370/484; 375/346; 379/56.2 ; 455/3.01 ; 455/3.05 ; 725/106; 725/114; 725/126; 725/129; 725/131	Dapper; Mark J. et al.
71	US 6606360 B1	20030812	19	Method and apparatus for receiving data	375/354		Dunning; David S. et al.
72	US 6606351 B1	20030812	173	Ingress protection in a communication system with orthogonal carriers	375/222		Dapper; Mark J. et al.
73	US 6603822 B2	20030805	173	Communicating errors in a telecommunications system	375/340	398/99	Brede; Jeffrey et al.
74	US 6603821 B1	20030805	19	Wireless communication terminal capable of correctly specifying position of burst and having small frequency error of recovered carrier	375/326	375/320; 375/340; 375/344	Doi; Masayuki
75	US 6603808 B1	20030805	23	Dual mode phone line networking modem utilizing conventional telephone wiring	375/222	709/200	Anne; Ramakrishna et al.



	Document ID	Issue Date	Page s	Title	Current OR	Current XRef	Inventor
76	US 6594322 B2	20030715	175	Method of distributed loop control for a multicarrier telephony transport	375/330	375/344	Dapper; Mark J. et al.
77	US 6580328 B2	20030617	8	Lock detector for phase locked loops	331/17	331/23; 331/25; 331/DIG .2; 375/340; 375/376	Tan; Loke Kun et al.
78	US 6574288 B1	20030603	53	Method and apparatus for adjusting a digital control word to tune synthesized high-frequency signals for wireless communications	375/327	455/262	Welland; David R. et al.
79	US 6570982 B1	20030527	27	Digital clocking synchronizing mechanism	379/327	370/509; 375/375	Weir; Stephen
80	US 6570930 B2	20030527	7	Three-state differential data transmission with self latching ability	375/295		Shepston; Shad R.
81	US 6570912 B1	20030527	13	Hybrid software/hardware discrete multi-tone transceiver	375/222	375/219; 375/220	Mirfakhraei; Khashayar

	Document ID	Issue Date	Page s	Title	Current OR	Current XRef	Inventor
82	US 6539051 B1	20030325	22	Parallel framer and transport protocol with distributed framing and continuous data	375/219	370/470; 370/474; 370/476; 375/222; 375/225; 375/257; 375/265; 375/295; 375/316	Grivna; Edward L.
83	US 6519275 B2	20030211	22	Communications system employing differential orthogonal modulation	375/140	375/145; 375/149; 375/150	Callaway, Jr.; Edgar H. et al.
84	US 6487264 B1	20021126	24	RF modem apparatus	375/361	340/825; 375/222; 375/282; 375/312; 455/343. 1	Alley; Daniel et al.
85	US 6466634 B1	20021015	58	Radio frequency data communications device	375/374	340/10.1	O'Toole; James E. et al.
86	US 6456608 B1	20020924	146	Adaptive vector correlator using weighting signals for spread-spectrum communications	370/335	370/342; 375/150	Lomp; Gary R.
87	US 6445731 B1	20020903	24	Method and apparatus for reducing signal processing requirements for transmitting packet-based data with a modem	375/222	379/93.0 9	Yamano; Larry C. et al.

	Document ID	Issue Date	Page s	Title	Current OR	Current XRef	Inventor
88	US 6434187 B1	20020813	28	Digital radiofrequency transceiver	375/219	332/128; 375/239; 375/297; 375/306; 375/326; 455/86	Beard; Paul F. et al.
89	US 6426972 B1	20020730	26	Reduced complexity equalizer for multi mode signaling	375/229	375/233	Endres; Thomas J. et al.
90	US 6400754 B2	20020604	75	Spread spectrum localizers	375/140	327/142; 327/160; 327/291; 331/173; 375/354; 713/400; 713/502	Fleming; Robert Alan et al.
91	US 6396887 B1	20020528	29	Apparatus and method for generating a distributed clock signal using gear ratio techniques	375/354	327/144; 375/371	Ware; Frederick A. et al.
92	US 6385268 B1	20020507	75	Spread spectrum localizers	375/377	343/905; 455/269	Fleming; Robert Alan et al.
93	US 6366599 B1	20020402	31	Fast acquisition of spread- spectrum signals by dynamically varying spacing of search bins	375/130		Carlson; Andrew B. et al.

	Document ID	Issue Date	Page s	Title	Current OR	Current XRef	Inventor
94	US 6334219 B1	20011225	180	Channel selection for a hybrid fiber coax network	725/106	341/100; 370/280; 375/235; 375/286	Hill; Terrance J. et al.
95	US 6333938 B1	20011225	43	Method and system for extracting control information from packetized data received by a communications interface device	370/503	375/240. 28	Baker; Richard T.
96	US 6330296 B1	20011211	11	Delay-locked loop which includes a monitor to allow for proper alignment of signals	375/376	327/159; 375/374	Atallah; Francois Ibrahim et al.
97	US 6320436 B1	20011120	12	Clock skew removal apparatus	327/158	327/159; 327/161; 327/271; 331/DIG .2; 375/373	Fawcett; Adrian et al.
98	US 6314128 B1	20011106	12	Spread spectrum synchronized receiver and synchronizing methods	375/149	375/147; 375/367	Bunker; Philip A. et al.
99	US 6310652 B1	20011030	16	Fine-grained synchronization of a decompressed audio stream by skipping or repeating a variable number of samples from a frame	348/515	370/509; 375/364; 704/503	Li; Stephen (Hsiao Yi) et al.
100	US 6304596 B1	20011016	22	Method and apparatus for reducing signal processing requirements for transmitting packet-based data with a modem	375/222	370/389	Yamano; Larry C. et al.
101	US 6298083 B1	20011002	33	Power savings technique for a positioning system receiver	375/150	342/357. 12; 375/130	Westcott; David C. et al.

	Document ID	Issue Date	Page s	Title	Current OR	Current XRef	Inventor
102	US 6292517 B1	20010918	37	Method and apparatus for detecting a data signal on a carrier medium	375/287	375/227	Jeffress; Jeremiah M. et al.
103	US 6285726 B1	20010904	13	10/100 mb clock recovery architecture for switches, repeaters and multi-physical layer ports	375/376	327/156; 327/163; 375/354; 375/373	Gaudet; Brian
104	US 6275990 B1	20010814	175	Transport of payload information and control messages on multiple orthogonal carriers spread throughout substantially all of a frequency bandwidth	725/106	375/260	Dapper; Mark J. et al.
105	US 6275685 B1	20010814	18	Linear amplifier arrangement	455/126	330/149; 375/296; 455/114.3; 455/63.1	Wessel; David N et al.
106	US 6275519 B1	20010814	37	Frame synchronization in a digital communications system	375/138	375/150	Hendrickson; Alan F.
107	US 6269130 B1	20010731	17	Cached chainback RAM for serial viterbi decoder	375/341	375/262; 714/794; 714/795	Hansquine; David
108	US 6263013 B1	20010717	38	Fast tracking of PN synchronization in a direct-sequence spread-spectrum digital communications system	375/150	375/132; 375/140	Hendrickson; Alan F.
109	US 6256335 B1	20010703	39	Slow tracking of PN synchronization in a direct-sequence spread-spectrum digital communications system	375/136	375/147	Hendrickson; Alan F.

	Document ID	Issue Date	Page s	Title	Current OR	Current XRef	Inventor
110	US 6239741 B1	20010529	10	UWB dual tunnel diode detector for object detection, measurement, or avoidance	342/135	342/118; 342/134; 342/175; 342/195; 342/21; 342/27; 342/70; 342/89; 375/295; 375/316; 375/340	Fontana; Robert J. et al.
111	US 6223317 B1	20010424	22	Bit synchronizers and methods of synchronizing and calculating error	714/744	375/362; 375/368; 714/798	Pax; George E. et al.
112	US 6215816 B1	20010410	38	Physical layer interface device	375/219	370/402; 375/220; 375/257; 375/258; 375/316	Gillespie; Alan et al.

	Document ID	Issue Date	Page s	Title	Current OR	Current XRef	Inventor
113	US 6177959 B1	20010123	15	Circuit and method for generating a clock signal synchronized with time reference signals associated with television signals	348/521	331/2; 331/20; 348/536; 348/537; 348/540; 348/541; 348/543; 348/544; 348/546; 375/327; 375/373; 375/375; 375/376	Bril; Vlad
114	US 6154468 A	20001128	20	Fast sync-byte search scheme for packet framing	370/510	370/514; 375/365	Lin; Chin-Sung et al.
115	US 6130921 A	20001010	15	Frequency shift modulation automatic frequency correction circuit and method	375/344	455/192. 2	Powell, II; Clinton C et al.
116	US 6125139 A	20000926	31	Narrowband digital cordless telephone	375/220	375/244; 455/511; 455/524; 455/553. 1	Hendrickson; Alan et al.
117	US 6075814 A	20000613	24	Method and apparatus for reducing signal processing requirements for transmitting packet-based data with a modem	375/222	455/574	Yamano; Larry C. et al.

	Document ID	Issue Date	Page s	Title	Current OR	Current XRef	Inventor
118	US 6049577 A	20000411	12	Header synchronization detector	375/368	370/514	Gothé; Marlo Rene et al.
119	US 6038267 A	20000314	39	Digital demodulator, maximum- value selector, and diversity receiver	375/329	329/304; 375/340; 375/347; 375/371	Oura; Hideto et al.
120	US 6018369 A	20000125	18	Video decoder with closed caption data on video output	348/461	348/465; 375/240. 27	Patterson; James T.
121	US 6002717 A	19991214	19	Method and apparatus for adaptive equalization using feedback indicative of undercompensation	375/232	375/233; 708/323	Gaudet; Brian
122	US 6002710 A	19991214	40	Timing recovery for a pseudo- random noise sequence in a direct-sequence spread-spectrum communications system	375/149	370/324; 370/350; 370/503; 370/507; 370/509; 370/512; 370/514; 370/515; 375/358; 375/359; 375/362; 375/367	Hendrickson; Alan F. et al.



	Document ID	Issue Date	Page s	Title	Current OR	Current XRef	Inventor
123	US 6002709 A	19991214	37	Verification of PN synchronization in a direct-sequence spread-spectrum digital communications system	375/150	370/350; 370/503; 370/514; 370/515; 370/516; 375/359; 375/362; 375/365; 375/367; 375/371; 375/373	Hendrickson; Alan F.
124	US 6002708 A	19991214	76	Spread spectrum localizers	375/130	327/336; 375/142; 375/343	Fleming; Robert Alan et al.
125	US 5995570 A	19991130	16	Recovering a clock signal in a multimedia network using time stamps	375/356	370/503	Onvural; Raif O. et al.
126	US 5982837 A	19991109	11	Automatic baud rate detector	375/377		Earnest; Tim
127	US 5969646 A	19991019	13	Apparatus and method for decoding differential multi-level data with adaptive threshold control	341/53	375/289	Cheng; Yi et al.
128	US 5956377 A	19990921	8	Method and apparatus for synchronizing frames within a continuous stream of digital data	375/372	370/514; 375/368	Lang; Ralph Uwe

	Document ID	Issue Date	Page s	Title	Current OR	Current XRef	Inventor
129	US 5950115 A	19990907	14	GHz transceiver phase lock loop having autofrequency lock correction	455/73	327/147; 327/148; 375/219; 375/327	Momtaz; Afshin D. et al.
130	US 5949817 A	19990907	23	Multi-level correlation system for synchronization detection in high noise and multi-path environments	375/142	375/343; 375/367	Marshall; Kenneth E.
131	US 5920572 A	19990706	29	Transport stream decoder/demultiplexer for hierarchically organized audio-video streams	370/535	348/476; 370/497; 370/522; 375/240. 25	Washington; Emanuel et al.
132	US 5905769 A	19990518	25	System and method for high-speed skew-insensitive multi-channel data transmission	375/376	327/156; 327/261	Lee; Kyeongho et al.
133	US 5892797 A	19990406	18	System and method for recovering data encoded using manchester code and other bi-phase level codes	375/361	329/304; 329/310; 341/68; 341/70; 375/279; 375/282; 375/329; 375/333; 375/359	Deng; Jay Jie

	Document ID	Issue Date	Page s	Title	Current OR	Current XRef	Inventor
134	US 5880973 A	19990309	16	Signal processing system and method for enhanced cascaded integrator-comb interpolation filter stabilization	702/190	375/350; 708/306; 708/313; 708/314	Gray; Joseph Harold et al.
135	US 5878221 A	19990302	101	Network for multimedia asynchronous transfer mode digital signal transmission and components thereof	709/218	370/488; 375/259	Szkopek; Lech A. et al.
136	US 5875217 A	19990223	52	Delay adjustment circuit in a performance monitoring and test system	375/372	370/516	Hartmann; Paul R. et al.
137	US 5870436 A	19990209	22	Uniform discrete fourier transform filter parameter encoder	375/316	370/210; 375/139	Kolaneck; James C. et al.
138	US 5838738 A	19981117	25	Coding to improve timing recovery in a sampled amplitude read channel	375/340	341/59; 360/51; 375/290	Zook; Christopher P.
139	US 5838730 A	19981117	164	Radio transmitter/receiver for wireless local area network	375/272	375/295; 455/86	Cripps; Peter K.
140	US 5835145 A	19981110	17	Conversion system using programmable tables for compressing transform coefficients	375/240. 2	341/65; 341/67	Ouyang; Chi et al.
141	US 5821886 A	19981013	17	Variable length code detection in a signal processing system	341/67	375/240. 01	Son; Jae Cheol
142	US 5818365 A	19981006	14	Serial to parallel conversion with phase locked loop	341/100	375/376	Hush; Glen et al.
143	US 5812594 A	19980922	31	Method and apparatus for implementing carrierless amplitude/phase encoding in a network	375/219	375/298; 375/340; 375/345; 375/355	Rakib; Selim

	Document ID	Issue Date	Page s	Title	Current OR	Current XRef	Inventor
144	US 5802118 A	19980901	43	Sub-sampled discrete time read channel for computer storage systems	375/350		Bliss; William G. et al.
145	US 5802103 A	19980901	29	High speed serial link for fully duplexed data communication	375/220	370/285	Jeong; Deog-Kyoon
146	US 5801782 A	19980901	19	Analog video encoder with metered closed caption data on digital video input interface	348/473	348/468; 375/240.01	Patterson; James T.
147	US 5799041 A	19980825	86	Network for multimedia asynchronous transfer mode digital signal transmission and components thereof	375/259	370/265; 370/420; 370/490; 725/118; 725/119; 725/95	Szkopek; Lech A. et al.
148	US 5796781 A	19980818	17	Data receiver having bias restoration	375/288	326/60; 375/229; 375/258; 375/319	DeAndrea; John J. et al.
149	US 5774505 A	19980630	12	Intersymbol interference cancellation with reduced complexity	375/348	360/46; 360/65; 375/233; 375/290	Baugh; Richard A.
150	US 5764687 A	19980609	26	Mobile demodulator architecture for a spread spectrum multiple access communication system	375/147	370/335; 375/349	Easton; Kenneth D.
151	US 5757869 A	19980526	24	Apparatus and method for detecting frame synchronization pattern/word in bit-stuffed digital data frame	375/366	370/506; 370/522; 375/368	Sands; Jeffrey J. et al.

	Document ID	Issue Date	Page s	Title	Current OR	Current XRef	Inventor
152	US 5748891 A	19980505	75	Spread spectrum localizers	375/150	342/132; 342/145; 342/189	Fleming; Robert Alan et al.
153	US 5740411 A	19980414	9	Controllably switched phase locked loop circuits, systems, and methods	713/503	331/1A; 331/25; 375/376	Hearn; Alan S. et al.
154	US 5729680 A	19980317	165	Ad hoc initialization for wireless local area network	709/222	370/436; 375/132; 709/248	Belanger; Philip H. et al.
155	US 5710793 A	19980120	29	Error signal quantization method and hardware for mixed blind and decision directed equalization	375/232	375/235; 708/323	Greenberg; Craig Bradley
156	US 5701330 A	19971223	11	Serial communication method and apparatus	375/257	340/534; 340/825. 5; 340/825. 52; 375/353	Lippmann; Raymond et al.
157	US 5699117 A	19971216	55	Moving picture decoding circuit	348/390. 1	375/240. 26	Uramoto; Shinichi et al.
158	US 5694586 A	19971202	12	Controller using time-domain filter connected to a signal line to control a time at which signal line is sampled for receipt of information transfer signal	713/401	375/350	Eneboe; Michael K.
159	US 5694438 A	19971202	17	Method and apparatus for managing a data symbol received in a time diversity communication system	375/347	711/200; 711/220	Wang; Annie Yalay et al.
160	US 5687326 A	19971111	17	Methods and apparatus for implementing a high speed serial link and serial data bus	710/105	375/220; 710/106	Robinson; Jeffrey I.

	Document ID	Issue Date	Page s	Title	Current OR	Current XRef	Inventor
161	US 5684828 A	19971104	26	Wireless data module with two separate transmitter control outputs	340/10.2	340/10.4 1; 340/10.5 ; 340/3.9; 375/219	Bolan; Michael L. et al.
162	US 5675584 A	19971007	30	High speed serial link for fully duplexed data communication	370/284	327/407; 327/415; 370/466; 370/503; 370/535; 375/220	Jeong; Deog- Kyoona
163	US 5654985 A	19970805	17	Address tracking over repeater based networks	375/211	340/825. 52; 370/246; 370/392; 375/213; 713/162; 713/163; 714/713	Crayford; Ian S. et al.
164	US 5654979 A	19970805	34	Cell site demodulation architecture for a spread spectrum multiple access communication systems	375/142	370/342; 375/150	Levin; Jeffrey A. et al.

	Document ID	Issue Date	Page s	Title	Current OR	Current XRef	Inventor
165	US 5654698 A	19970805	168	Missile telemetry data interface circuit	340/870.01	340/825.64; 340/870.18; 340/870.19; 375/242; 455/98	Snelgrove; Andrew H. et al.
166	US 5651001 A	19970722	22	Method and apparatus for full duplex signaling	370/276	370/226; 370/279; 370/285; 375/288	Alvstad; Gary A. et al.
167	US 5644361 A	19970701	16	Subsampled frame storage technique for reduced memory size	375/240.16	375/240.21	Ran; Xiaonong et al.
168	US 5636294 A	19970603	29	Method and apparatus for truncation of images during data compression	382/239	358/426.05; 358/426.06; 375/240.03; 375/240.05; 375/240.2	Grosse; Debora I. Y. et al.
169	US 5630033 A	19970513	91	Adaptive threshold filter and method thereof	345/418	345/501; 348/699; 375/240.15	Purcell; Stephen C. et al.
170	US 5627860 A	19970506	24	Peak and valley detector for a selective call receiving device	375/317	340/7.43; 375/316; 375/340	McKinny; Carol A. et al.

	Document ID	Issue Date	Page s	Title	Current OR	Current XRef	Inventor
171	US 5608755 A	19970304	36	Method and apparatus for implementing carrierless amplitude/phase encoding in a network	375/219	370/201; 375/261	Rakib; Selim
172	US 5598429 A	19970128	15	Multi-level correlation system for synchronization detection in high noise and multi-path environments	375/149	370/515; 375/150; 375/367	Marshall; Kenneth E.
173	US 5587709 A	19961224	29	High speed serial link for fully duplexed data communication	341/100	375/371	Jeong; Deog-Kyoon
174	US 5581302 A	19961203	15	Subsampled frame storage technique for reduced memory size	375/240. 16	348/699; 375/240. 21	Ran; Xiaonong et al.
175	US 5557616 A	19960917	53	Frame synchronization in a performance monitoring and test system	370/509	375/357; 375/368	Cadieux; Kevin et al.
176	US 5537435 A	19960716	57	Transceiver apparatus employing wideband FFT channelizer with output sample timing adjustment and inverse FFT combiner for multichannel communication network	375/219	370/210; 370/280; 370/484; 375/232; 375/233; 375/260	Carney; Ronald et al.
177	US 5530483 A	19960625	106	Delay detector apparatus and method for plural image sequences	348/518	348/192; 375/240. 22; 382/209; 714/815	Cooper; James C. et al.
178	US 5524008 A	19960604	9	Generating real-time events in a TDMA digital wireless communications system	370/337	375/219	Levy; Jonathan



	Document ID	Issue Date	Page s	Title	Current OR	Current XRef	Inventor
179	US 5500853 A	19960319	63	Relative synchronization system for a telephone network	370/252	370/520; 375/357; 379/14.0 1; 714/712	Engdahl; Thomas L. et al.
180	US 5495470 A	19960227	64	Alarm correlation system for a telephone network	370/248	370/252; 370/516; 375/362; 379/14; 379/15.0 5; 379/33; 714/712	Tyburski; Edward et al.
181	US 5495469 A	19960227	21	Communications network, state machine therefor	370/212	340/3.1; 370/462; 375/238	Halter; Richard A. et al.
182	US 5488641 A	19960130	12	Digital phase-locked loop circuit	375/374	327/150; 370/518; 375/376	Ozkan; Oguz
183	US 5483539 A	19960109	19	Programmable PCM/TDM demultiplexer	370/509	370/514; 370/542; 375/368	Kaufmann; John
184	US 5471508 A	19951128	41	Carrier recovery system using acquisition and tracking modes and automatic carrier-to-noise estimation	375/344	375/261; 455/226. 3	Koslov; Joshua L.

	Document ID	Issue Date	Page s	Title	Current OR	Current XRef	Inventor
185	US 5463662 A	19951031	20	Apparatus and method for reducing errors in data caused by noise through use of blanking	375/351	455/223	Sutterlin; Philip H. et al.
186	US 5448593 A	19950905	30	Frequency hopping time-diversity communications systems and transceivers for local area networks	375/267	370/389; 375/295; 375/345; 455/226.1; 714/704	Hill; Lawrence W.
187	US 5440591 A	19950808	10	Switching apparatus for digital signals	375/354	340/825.2; 370/517	Liron; John E. et al.
188	US 5421023 A	19950530	51	Motion vector calculation method using sequential minimum distortion calculations at different densities	348/701	375/240.16	Murakami; Tokumichi
189	US 5410572 A	19950425	21	Phase locked loop circuit	375/376	331/14	Yoshida; Koichi
190	US 5406590 A	19950411	11	Method of and apparatus for correcting edge placement errors in multiplying phase locked loop circuits	375/376	327/159; 327/175; 331/10; 714/700	Miller; Joseph P. et al.
191	US 5402448 A	19950328	14	Burst mode receiver control	375/340	370/514; 375/376	Marko; Paul D. et al.
192	US 5400369 A	19950321	9	Fram aligner with reduced circuit scale	375/368	370/510	Ikemura; Kuniichi
193	US 5400087 A	19950321	102	Motion vector detecting device for compensating for movements in a motion picture	348/699	375/240.15; 375/240.16	Uramoto; Shinichi et al.

	Document ID	Issue Date	Page s	Title	Current OR	Current XRef	Inventor
194	US 5394444 A	19950228	19	Lock detect circuit for detecting a lock condition in a phase locked loop and method therefor	375/374	327/156; 375/376	Silvey; John M. et al.
195	US 5325405 A	19940628	16	Burst mode receiver control	375/366	370/509	Marko; Paul D. et al.
196	US 5309428 A	19940503	56	Token ring local area network testing apparatus for phase jitter testing	370/245	370/452; 370/518; 375/226; 714/715; 714/717	Copley; Mark et al.
197	US 5260842 A	19931109	23	Data separator having an accurate delay circuit	360/51	360/48; 360/53; 375/359	Leake; William W. et al.
198	US 5258999 A	19931102	17	Circuit and method for receiving and transmitting control and status information	375/220	375/377	Wernimont; Thomas L. et al.
199	US 5258877 A	19931102	19	Data separator having a restart circuit	360/51	360/53; 375/376	Leake; William W. et al.
200	US 5179577 A	19930112	17	Dynamic threshold data receiver for local area networks	375/317	327/72; 455/219	Ilyadis; Nicholas
201	US 5115451 A	19920519	11	Local area network modem	375/221	370/249; 370/516; 375/355	Furlong; Darrell
202	US 5111451 A	19920505	16	Method and apparatus for synchronizing an optical transceiver over a full duplex data communication channel	370/294	370/516; 375/222; 375/376	Piasecki; Douglas S. et al.

	Document ID	Issue Date	Page s	Title	Current OR	Current XRef	Inventor
203	US 5050189 A	19910917	25	Multibit amplitude and phase modulation transceiver for LAN	375/223	370/516; 375/263; 375/290; 375/348; 375/371	Cox; William M. et al.
204	US 5036528 A	19910730	12	Self-calibrating clock synchronization system	375/374	327/231; 327/3; 331/18	Le; Duc N. et al.
205	US 4989221 A	19910129	12	Sample rate converter	375/222	370/290; 379/406.08	Qureshi; Shahid U. H. et al.
206	US 4951139 A	19900821	65	Computer-based video compression system	375/240.07	375/245	Hamilton; Eric R. et al.
207	US 4945548 A	19900731	34	Method and apparatus for detecting impending overflow and/or underrun of elasticity buffer	375/214	370/506; 375/372	Iannarone; John R. et al.
208	US 4897717 A	19900130	66	Computer-based video compression system	375/240.07	375/240; 375/240.22	Hamilton; Eric R. et al.
209	US 4873703 A	19891010	33	Synchronizing system	375/371	375/354; 375/356; 713/400; 713/501	Crandall; Douglas et al.

	Document ID	Issue Date	Page s	Title	Current OR	Current XRef	Inventor
210	US 4829258 A	19890509	17	Stabilized phase locked loop	327/156	327/157; 327/159; 327/227; 331/1A; 331/25; 375/376	Volk; Andrew M. et al.
211	US 4819081 A	19890404	17	Phase comparator for extending capture range	327/12	327/159; 327/237; 327/3; 331/1A; 375/373; 375/376	Volk; Andrew M. et al.
212	US 4766606 A	19880823	53	Signal repeater for multi subscriber communication over single pair telephone line	379/344	370/293; 375/214	Bardutz; Ronald W. et al.
213	US 4698672 A	19871006	19	Coding system for reducing redundancy	375/240. 12	358/1.9; 375/246	Chen; Wen- hsiung et al.
214	US 4680778 A	19870714	15	Fast acquisition circuit for synchronous digital receiver operating in wideband noise	375/344	375/376; 455/265	Krinock; Jerome V.
215	US 4592072 A	19860527	15	Decoder for self-clocking serial data communications	375/282	329/311; 360/42; 375/328; 375/333; 375/360	Stewart; Robert E.

	Document ID	Issue Date	Page s	Title	Current OR	Current XRef	Inventor
216	US 4580134 A	19860401	36	Color video system using data compression and decompression	345/589	345/605; 375/240. 24	Campbell; Graham M. et al.
217	US 4571735 A	19860218	20	Method of multi-level encoding including synchronizing signals	375/293	341/57; 341/94; 360/40; 375/292; 714/810	Furse; Anthony G.
218	US 4528676 A	19850709	17	Echo cancellation circuit using stored, derived error map	375/232	327/552; 332/107; 333/18; 375/231; 375/293; 375/348	Mein; Gordon F. et al.
219	US 4501003 A	19850219	174	Dial pulse measurement circuitry	375/340	379/351; 714/715; 714/761	Miller; Hubert A.
220	US 4451916 A	19840529	99	Repeatered, multi-channel fiber optic communication network having fault isolation system	714/4	370/226; 375/214; 380/2; 380/256; 380/268; 398/5; 455/16; 455/8; 455/9	Casper; Paul W. et al.
221	US 4450572 A	19840522	15	Interface for serial data communications link	375/333	360/44; 375/361	Stewart; Robert E. et al.

	Document ID	Issue Date	Page s	Title	Current OR	Current XRef	Inventor
222	US 4392159 A	19830705	79	Method and apparatus for digital video signal processing	386/12	348/500; 348/521; 360/32; 375/363; 386/40	Lemoine; Maurice G. et al.
223	US 4100533 A	19780711	28	Multipoint polling technique	340/825. 22	178/3; 340/825. 52; 340/825. 53; 370/449; 375/222	Napolitano; Orlando et al.
224	US 3944940 A	19760316	7	Versatile phase-locked loop for read data recovery	360/51	331/11; 331/27; 360/73.0 1; 375/327; 375/376	Desai; Ashok K.
225	US 3818347 A	19740618	18	RECEIVER FOR AMPLITUDE MODULATED QUADRATURE CARRIER SIGNALS	375/340	375/327; 375/376	Holsinger; Jerry L.